

PRINTER RUSH

(PTO ASSISTANCE)

Application : 10/162320 Examiner : Monestime GAU : 2676
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[RUSH] MESSAGE:

① Within the brief description & drawings in the specification IIC is replaced by & C. which is already listed. Please advise.

Thanks

[XRUSH] RESPONSE:

Typo corrected.

Thanks.

INITIALS: JBH

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

[0022] Fig. 9 is a block diagram illustrating the dual-cache method of copying a portrait-oriented graphic from system memory to the landscape-oriented frame buffer;

[0023] Fig. 10A is a block diagram illustrating the operation of the dual-cache method wherein, after reading the nth pixel (AE), the CPU then begins processing the sub-columns of other pixels already resident in the L2 Cache (requiring no L2 Cache load but, instead, a WC Cache write to the frame buffer) by reading the first pixel (BA) in the second sub-column;

[0024] Fig. 10B is a block diagram illustrating the operation of the dual-cache method wherein, after reading the first pixel in the second sub-column (BA), reading the second pixel (BB) in the second sub-column (requiring neither a L2 Cache load nor a WC Cache write);

[0025] Fig. 11A is a logical representation of a display on a display device in a landscape orientation to mirror the memory arrangement of the landscape-oriented frame buffer ("default frame buffer display") as displayed using the dual-cache method;

[0026] Fig. 11B is a logical representation of a display on a display device in a portrait orientation corresponding to the portrait-orientation of the graphic being displayed ("portrait graphic") as displayed using the dual-cache method;

JBH
7-29-05 [0027] Fig. 8C is a block diagram illustrating the rasterized pattern of the pixels as drawn (by the dual-cache method) on the display device in a portrait orientation;

[0028] Fig. 12 is matrix illustrating rough estimates of performance gains based on the "slow operation" presumptions to highlight the benefits of the dual-cache method compared to the write-combine method; and

[0029] Fig. 13 is flowchart illustrating the method of one embodiment of the invention in its simplest form.

DETAILED DESCRIPTION OF ILLUSTRATIVE EMBODIMENTS

[0030] The subject matter is described with specificity to meet statutory requirements. However, the description itself is not intended to limit the scope of this patent. Rather, the inventors have contemplated that the claimed subject matter might also be embodied in other ways, to include different steps or combinations of steps similar to the ones described in this document, in conjunction with other present or future technologies. Moreover, although the term "step" may be used herein to connote different elements of methods employed, the term should